

LSI LOGIC

LSI Logic Corporation
Legal Department - IP
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Milpitas, CA 95035

Facsimile Transmittal Letter

Date April 26, 2005Number of pages including cover sheet 3

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From: Connie del Castillo

Intellectual Property Paralegal

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REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please comment

Application Number: 09/851,181
Filing date: May 18, 2001
First named inventor: Theodore Vaida et al.
Attorney docket number: 01-035 / LSIL-01-035

Transmitted herewith for filing via facsimile:

- Information Disclosure Statement Letter
- Information Disclosure Statement by Applicant PTO/SB/O&A

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

Date 4/26/05

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Signature Connie del Castillo

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Theodore Vaida et. al.

Serial No. : 09/851,181

Filed : May 08, 2001

For : Application Specific Integrated
Circuit Having A Programmable
Logic Core And A Method Of
Operation Thereof

Group Art Unit : 2661

Examiner : Phan, Tri H.

Atty Docket : LSIL-01-035 / 01-035

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

4/26/05 *Connie de la Cruz*
Date Signature

Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milpitas, CA 95035
408-433-7475

Date: 4/25/2005

Respectfully submitted,

Henry Groth

Henry Groth

Reg. No. 39,696

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PTO/SB/08A (10-98)

Approved for use through 10/31/99. OMB 0851-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		<i>Complete if Known</i>			
		Application Number	09/851,181		
		Filing Date	May-08, 2001		
		First Named Inventor	Theodore Vaida		
		Group Art Unit	2661		
		Examiner Name	Tri H. Phan		
Sheet	1	of	1	Attorney Docket No.	LSIL-01-035 / 01-035

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code (if known)		
		6,701,432		Deng et al.	03-02-2004
		6,347,347		Brown et al.	02-12-2002
		6,393,457		Allison et al.	05-21-2002
		6,226,292		DiPlacido	05-01-2001
		6,205,509		Platko et al.	03-20-2001
		6,807,581		Starr et al.	10-19-2004

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Examiner signature		Date considered	
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Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Department of Commerce, Room 3000, 1400 Constitution Avenue, NE, Washington, DC 20503. FAX TO: 202-473-2000.